

## IN THE ABSTRACT:

The Abstract as amended below with a replacement Abstract shows amended text with underlining and deleted text with ~~strikethrough~~.

~~The present invention relates to a~~ processing apparatus ~~comprised~~  
~~with~~including an internal circuit having a CPU and internal devices and an external circuit  
including external devices provided externally of the internal circuit, and the like, and is aimed to  
prevent illicit access and reverse engineering. The ~~present invention comprises an internal~~  
circuit 400 including a CPU 401, internal devices 402 ~~to 405~~ and a bus line 440 connecting the  
CPU 401 to the internal devices 402 ~~to 405~~ and extending externally, and ~~an the~~ external circuit  
200 including external devices 201 ~~to 203~~ and 211 ~~to 212~~ provided externally of an externally  
extending portion 440a of the bus line 440. The internal circuit 400 ~~further includes~~ including a  
ciphering section 120 provided at an entrance to an external side and ciphering addresses and  
data on the bus line 440 by ciphering patterns according to a plurality of regions divided from an  
address space allotted to the entire external devices.